

# OC-768 CLOCK OSCILLATOR

Model FO-CL-40-00

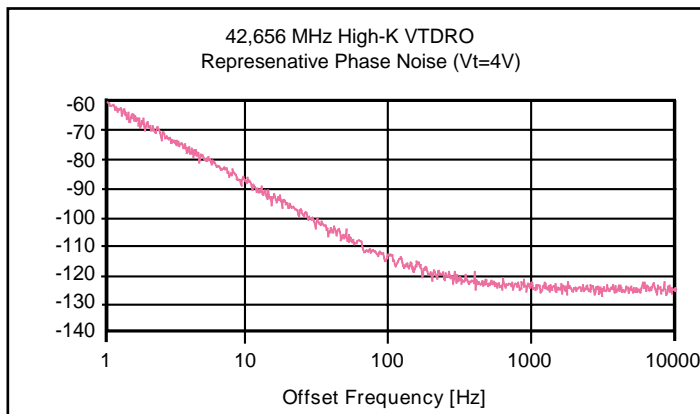
**Applications:** Designed for critical timing applications required in SONET/SDH OC-768 systems.

**Features:**

- ◆ Ultra-Stable DRO Technology
- ◆ Lowest Phase Noise / Jitter
- ◆ Minimal DC Power Dissipation
- ◆ Low Cost SMT Construction

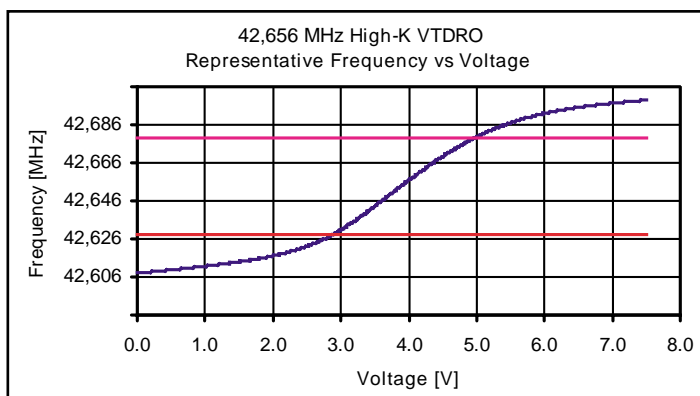
**Phase Noise, Max (-30° to +70°C):**

1 kHz offset	-55 dBc/Hz
10 kHz offset	-85 dBc/Hz
100 kHz offset	-110 dBc/Hz
1 MHz offset	-130 dBc/Hz



**Output Characteristics:**

Typical Frequency:	39 to 44 GHz
Typical Tuning Range:	25 to 50 MHz
Tuning Voltage:	3 to 5 Volts (High K) 1 to 10 Volts (Low K)
Modulation Sens:	24 MHz/V Typ. (High K) 4 MHz/V Typ. (Low K)
Output Power:	+12 dBm, Typ. (Other power levels available)



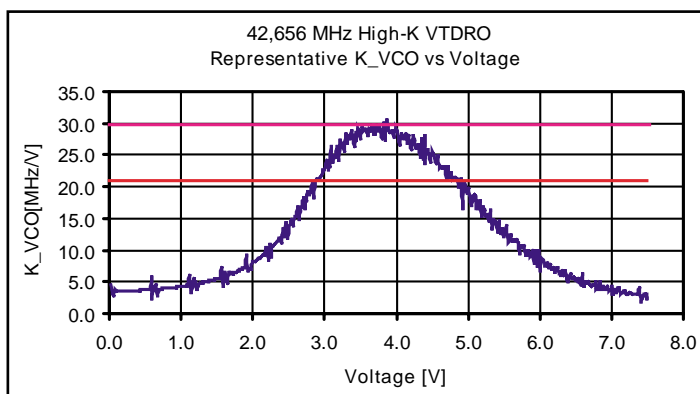
**Connectors:**

RF: "V" Connector (Alternative connectors available)

DC: Solder Pin

**DC Power:**

+8 to +12V @ 225 mA  
-8 to -12V @ 15 mA  
Internally Regulated

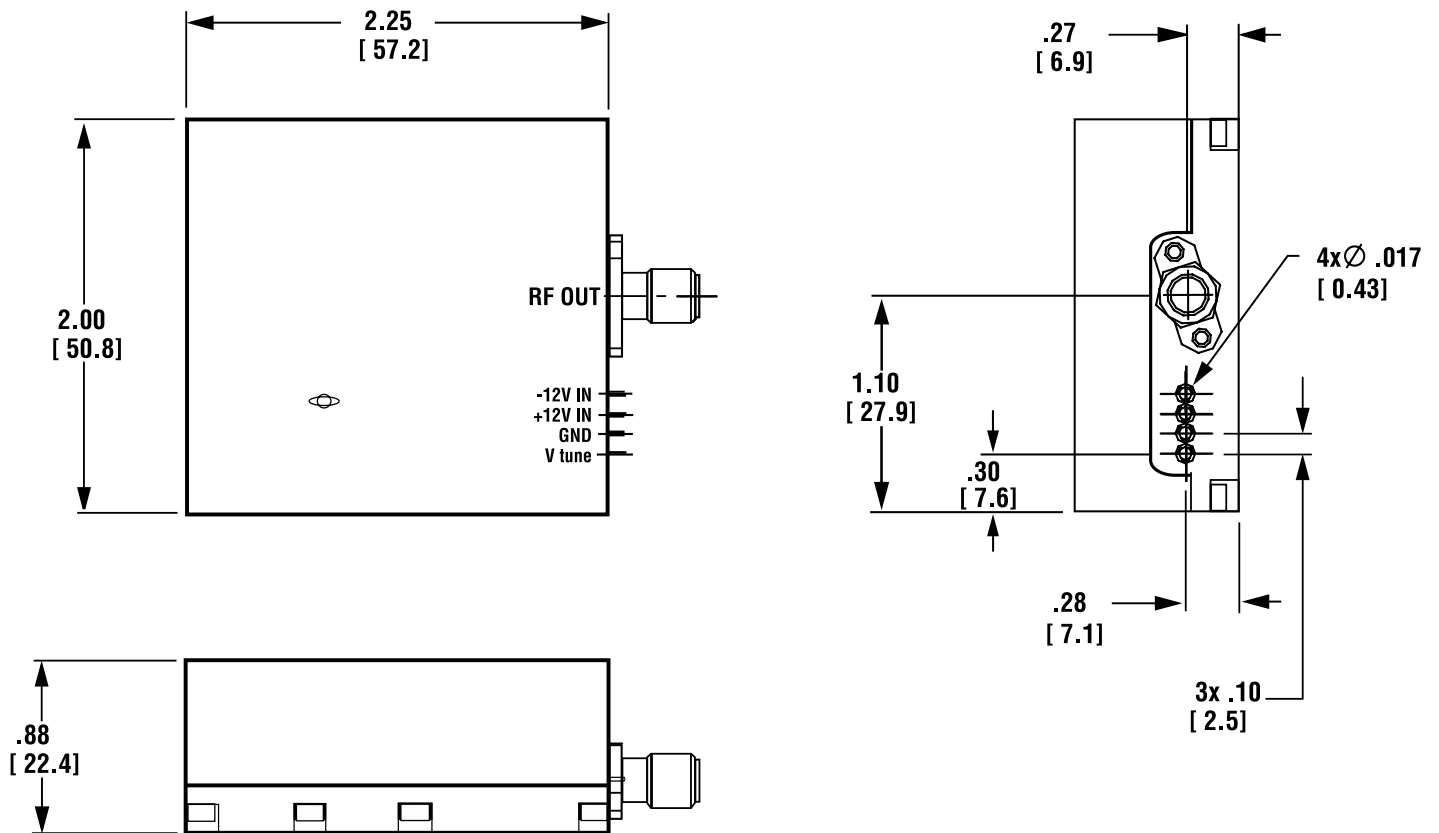


an L-3 communications company

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## Outline Drawing



Contact Narda for your custom requirements.

L-3 Communications has patent applications pending which protects this and related fiber optic products.

These products may also be protected by other intellectual property rights including trademarks and copyrights.

**narda**

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